

## CLAIMS

1. A system for anti-theft transponders, **characterized by** an autonomous low power transmitter device capable of transmitting  
5 a specified command for accessing at least one anti-theft transponder, the anti-theft transponder comprising one receiver module operating on a designated frequency used by a general coverage paging system, the transponder further comprising at least one transmitting means and control circuitry, and

10 that the anti-theft transponders are provided with a first individual access identity code for authorization of an activation by means of a paging system and at least a second general access identity code for a direct control by the autonomous low power transmitter within its limited coverage.

15 2. The system according to claim 1, **characterized in** that the autonomous low power transmitter by means of a manual activating switch (S2) initiates a transmitting of a defined series of transmissions of an authorization code for the at least second general access identity code of transponders in the anti-theft transponder system to thereby  
20 control each transponder receiving the transmissions of the autonomous low power transmitter.

3. The system according to claim 2, **characterized in** that the autonomous low power transmitter utilizes a common signal format, e.g a POCSAG code or similar, to produce the at least second general access identity code for the autonomous low power transmitter for control of each anti-theft transponder within a limited coverage of the autonomous low power transmitter when the autonomous low power transmitter is triggered to operate.

39 4. A transmitter for anti-theft transponders for locally controlling the anti-transponders for a change into an active mode, **characterized in**

an autonomous low power transmitter device is formed capable of generating a specified command for accessing at least one anti-theft of the transponders, the anti-theft transponders comprising a receiver module operating on a designated frequency used by a general coverage paging system, and the anti-theft transponders are provided with a first individual access identity code for authorization of an activation by means of a paging system and at least a second general access identity code for a direct control by the autonomous low power transmitter within its limited coverage area, and

that the autonomous low power transmitter is provided with a control switch (S2) for activating a broadcast of a defined series of transmissions with an authorization code for anti-theft transponders in a transponder system to thereby locally control each transponder receiving the transmissions of the autonomous low power transmitter.

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5. The transmitter device according to claim 4, **characterized in** that the defined series of transmission has a repetition rate during a predetermined time period after initialization in order to guarantee a proper interlacing of transmissions by a general coverage paging system normally controlling activation of the transponder.

20 6. The transmitter device according to claim 5, **characterized in** that the autonomous low power transmitter transmits an authorization code being common for all transponders in an anti-theft transponder system to control all transponders within the coverage area of the low power transmitter.

25 7. The transmitter device according to claim 6, **characterized in** that a transmit frequency of the autonomous low power transmitter is the same as a communication frequency used for a paging system utilized for controlling the transponders of an anti-theft system.

8. The transmitter device according to claim 7, **characterized in** that the autonomous low power transmitter utilizes a POCSAG code or any corresponding protocol for transferring at least one general Receiver

5 Identification Code, RIC, to transponders within the limited coverage area of the transmitter to thereby control the transponders receiving signals from the autonomous low power transmitter when it is initialized.

9. The transmitter device according to claim 8, **characterized in** that

10 the microprocessor (12) of the autonomous low power is provided with a fourth general RIC which can be transmitted in order to reset local transponders of the anti-theft system to a standby state.